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EDA CONSORTIUM APPOINTS MIPS TECHNOLOGIES CEO TO BOARD

SAN JOSE, California, June 13, 2005— The Electronic Design Automation (EDA) Consortium announced today that it has appointed John Bourgoïn to its board of directors to serve the organization through April 2006. Bourgoïn is Chief Executive Officer and President, MIPS Technologies, Inc., a leading provider of industry standard processor architectures and cores for digital consumer and business applications.

Walden C. Rhines, chairman of the Consortium and chairman and CEO of Mentor Graphics Corporation commented, "John Bourgoïn's appointment to our board, as a highly respected CEO within the IP industry, is a demonstration of our commitment to making the EDA Consortium represent the entire electronic design industry. We welcome Mr. Bourgoïn to the board, as someone who will help the Consortium reflect the close alignment between IP and EDA."

Mr. Bourgoïn brings more than 30 years of semiconductor experience to his role, drawing from past roles in business management and microprocessor development at MIPS Technologies and AMD, and earlier, in engineering management at Motorola Semiconductor Products Division in Phoenix. Previously, Mr. Bourgoïn was senior vice president of Silicon Graphics, Inc., and president of the MIPS Group of SGI. Prior to that, he was group vice president of the Computation Products Group at Advanced Micro Devices, Inc.

Bourgoïn stated, "I'm very pleased to be named to the board of directors of the EDA Consortium. I believe that the IP and EDA businesses have many common issues, and I look forward to helping the Consortium address them."

Other members of the EDA Consortium board are: Walden C. Rhines, Chairman and CEO, Mentor Graphics Corporation; Ray Bingham, Executive Chairman, Board of Directors, Cadence Design Systems Inc.; Alan P. Naumann, President and CEO, CoWare, Inc.; Jacques Benkoski, CEO and President, Monterey Design Systems; Aart de Geus, PhD, Chairman and CEO, Synopsys, Inc.; Jim Douglas, President and CEO ReShape, Inc.; Kathryn Kranen, President and CEO, Jasper Design Automation, and Sanjay Srivastava, President and CEO, Denali Software, Inc.

About MIPS Technologies, Inc.

MIPS Technologies, Inc. is a leading provider of industry-standard processor architectures and cores for digital consumer and business applications. The company drives the broadest architectural alliance that is delivering 32- and 64-bit embedded RISC solutions. The company licenses its intellectual property to semiconductor companies, ASIC developers, and system OEMs. MIPS

Technologies, Inc. and its licensees offer the widest range of robust, scalable processors in standard, custom, semi-custom and application-specific products. MIPS Technologies, Inc. is based in Mountain View, California, and can be reached at 650-567-5000 or www.mips.com.

About the EDA Consortium

The Electronic Design Automation (EDA) Consortium is the international association of companies that provide tools and services that enable engineers to create the world's electronic products. Electronic design is the critical technology used to design electronics for the communications, computer, space technology, medical and industrial equipment and consumer electronics markets among others. The EDA Consortium produces the Market Statistics Service (MSS), which is the source of quarterly, detailed EDA and IP revenue data reporting. This valuable report is used by industry stakeholders to better track product category trends, develop business forecasts, and measure market growth. For more information about the EDA Consortium or to subscribe to the Market Statistics Service, contact EDA Consortium, 111 West Saint John Street, Suite 220, San Jose, Calif. 95113, USA, office 408-287-3322, fax 408-283-5283 or visit www.edac.org.

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