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**Dr. Aart de Geus Selected to Receive the 15th Annual Phil Kaufman Award
by the EDA Consortium and the IEEE Council on EDA**

*Synopsys Co-Founder, Chairman and CEO recognized for his
technical and business impact on the EDA industry*

San Jose, CA– September 15, 2008 – The Electronic Design Automation (EDA)

Consortium and IEEE Council on EDA (CEDA) today announced that Dr. Aart de Geus, co-founder, chairman and CEO of Synopsys, Inc., has been chosen as this year’s recipient of the EDA industry’s prestigious Phil Kaufman Award for Distinguished Contributions to EDA. The EDA Consortium and Council on EDA will jointly present the Award to Dr. de Geus on Wednesday, October 29, 2008, at its 15th annual Kaufman Award dinner and ceremony at the Doubletree Hotel in Santa Clara, Calif.

Since co-founding Synopsys in 1986, Dr. de Geus has expanded Synopsys from a start-up synthesis enterprise to a world leader in EDA and a major force in semiconductor IP. Under his leadership, Synopsys instituted the ratable business model, leading the software industry to a more stable, predictable business model.

“It is hard to imagine an EDA tool that has helped the electronics industry more in keeping pace with advancing silicon than logic synthesis,” said John Darringer, President of the IEEE Council on EDA (CEDA). “The Council on EDA is delighted to join in recognizing Aart de Geus for making logic synthesis a part of everyday design and for the vital impact on design productivity.”

Wally Rhines, Chairman of the EDA Consortium and Chairman and CEO of Mentor Graphics, added “Aart has had major impacts on both the business, as well as the technology, of EDA. As a pioneer in logic synthesis, he found the path to commercialize one of EDA’s most important capabilities and, by building upon it, created the industry’s largest EDA company.”

Kurt Keutzer, Professor of EECS at the University of California, Berkeley and former CTO of Synopsys, said “Aart’s research contributions formed a critical piece of the technical foundation of the Design Compiler tool, probably the most successful product in the history of the EDA industry. As founder of Synopsys in 1986 and CEO since 1994, Aart led Synopsys to preeminence in the industry. In today’s market, Synopsys’ market capitalization of close to \$3B puts it head and shoulders above its competitors. This unique combination of technical and business impact makes Aart an ideal Kaufman Award recipient.”

Synopsys develops and offers some of the world’s most sophisticated software. Under Dr. de Geus’ leadership, the company has introduced several breakthrough technologies to the industry, including: static timing analysis; automatic scan-based test techniques and synthesis; physical synthesis employing actual silicon effects during synthesis; and a broad portfolio of IP cores that facilitate design reuse.

Dr. de Geus has received many prestigious technology awards including being named a Fellow of the Institute of Electrical and Electronics Engineers (IEEE) in January 1999. He was also honored for pioneering the commercial logic synthesis market with the IEEE Circuits and Systems Society Industrial Pioneer Award in 2001, as well as for his “contributions to, and leadership in, the technology and business development of Electronic Design Automation” with the 2007 IEEE Robert N. Noyce Medal.

Dr. de Geus has been honored for his innovative business leadership as well. In 2002, shortly after transacting the largest merger in EDA history, Dr. de Geus was named CEO of the Year by Electronic Business magazine. In November 2005, Electronic Business magazine selected Dr. de Geus as one of “The 10 Most Influential Executives.”

Dr. de Geus is active in the business community as a member of the board of Applied Materials, Silicon Valley Leadership Group, TechNet, the Global Semiconductor Alliance (GSA; formerly FSA), and as Vice-Chairman of the EDA Consortium. He is also a very active leader in education and community support, having created the Synopsys Outreach Foundation, which has promoted project-based science and math learning throughout Silicon Valley since 1999. Dr. de Geus was former Chair and a current board member of the Silicon Valley Leadership Group, and was also the recipient of its prestigious “Spirit of Silicon Valley Lifetime Achievement Award”.

Dr. de Geus received his master’s of science in electrical engineering from the Swiss Federal Institute of Technology in Lausanne, and his Ph.D. in electrical engineering from Southern Methodist University in Dallas, Texas.

About the Kaufman Award

Presented annually since 1994, the award honors an individual who has had a demonstrable impact on the field of EDA. It was established in honor of deceased EDA industry pioneer Phil Kaufman, who turned innovative technologies such as silicon compilation and emulation into businesses that have benefited electronic designers. For more information on the award, go to www.edac.org or www.ieee-ceda.org.

About the EDA Consortium

The EDA Consortium is the international association of companies that provide tools and services that enable engineers to create the world’s electronic products. EDA is the critical

technology used to design electronics for the communications, computer, space technology, medical and industrial equipment and consumer electronics markets among others. For more information about the EDA Consortium visit www.edac.org.

About the Council on EDA

The Council on Electronic Design Automation is IEEE's focal point for multiple EDA disciplines. Its goal is to bring increased value to IEEE members and the EDA community as a whole by coordinating EDA activities, enabling new initiatives, fostering interdisciplinary research and recruiting young talent to EDA. It also will increase visibility for IEEE-sponsored EDA events such as the Design Automation Conference (DAC) and International Conference on Computer Aided Design (ICCAD) and its technical publications. Its charter spans theory, implementation and use of computer aided design (CAD) tools to design integrated electronic circuits and systems. Its website is located at: <http://www.ieee-ceda.org>.

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