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Atrenta CEO Joins the EDA Consortium Board of Directors

SAN JOSE, Calif., January 29, 2015 – The Electronic Design Automation (EDA) Consortium announced today that it has appointed Atrenta Inc. chairman, president and CEO, Dr. Ajoy Bose, to its board of directors to serve the organization through the spring of 2016.

“Atrenta has been a valuable member of the EDA Consortium for many years, and we are pleased that Dr. Bose has joined the board of directors,” said EDAC executive director, Robert Gardner. “Adding Ajoy's significant experience and insight to the board will help guide the EDA Consortium and the industry in the coming years.” Ajoy joins the current EDAC board of directors, including:

- **John Kibarian**, EDA Consortium co-chairman, and president, CEO and co-founder of PDF Solutions, Inc.
- **Lip-Bu Tan**, EDA Consortium co-chairman, and president and CEO of Cadence Design Systems, Inc.
- **Aart de Geus**, EDA Consortium director, and chairman and CEO of Synopsys, Inc.
- **Dean Drako**, EDA Consortium director, and president and CEO of IC Manage, Inc.
- **Grant Pierce**, EDA Consortium director, and president and CEO Sonics, Inc.
- **Walden C. Rhines**, EDA Consortium director, and chairman and CEO of Mentor Graphics Corporation
- **Simon Segars**, EDA Consortium director, and CEO of ARM

“I am honored to join the board of directors of the EDA Consortium,” said Dr. Bose. “Atrenta has been a consistent supporter and beneficiary of EDAC's activities, including the efforts of the EDAC committees, educational activities, the Phil Kaufman Award, and co-sponsorship of the Design Automation Conference. EDAC continues to provide an effective forum to address common issues facing EDA and IP companies, including licensing, export controls, and others which are best addressed jointly, allowing member companies to focus resources on their unique products.”

About the EDA Consortium

The EDA Consortium is the international association of companies that provide tools and services that enable engineers to create the world's electronic products. EDA is the critical technology used to design electronics for the communications, computer, space technology, medical and industrial equipment and consumer electronics markets among others. For more information about the EDA Consortium visit www.edac.org.

About Ajoy Bose

Dr. Bose founded Atrenta Inc. in 2001. He began his career at AT&T Bell Laboratories in Murray Hill, N.J. and spent 12 years in a series of increasingly responsible positions ending as Department Head, Integrated Circuit Design Automation Department. From AT&T, he moved to Cadence Design Systems where he served as Vice President of Engineering. In this role, he managed and led the team that pioneered the Verilog simulation products. Prior to establishing Atrenta, Dr. Bose was founder and president of Software & Technologies Inc. and Interra Inc. During his tenure as Chairman and CEO of Interra, Dr. Bose incubated and spun out a number of successful ventures in EDA, Digital Video, and IT services. Dr. Bose received a Bachelors degree in Electrical Engineering from the Indian Institute of Technology in Kanpur, India which has awarded him the Distinguished Alumni award. He earned his MS and PhD in Electrical and Computer Engineering from the University of Texas at Austin.

About Atrenta

Atrenta's SpyGlass Predictive Analyzer® gives engineers a powerful guidance dashboard that enables efficient verification and optimization of SoC designs early, before expensive and time-consuming traditional EDA tools are deployed.

The company is headquartered in San Jose, California, with sales offices, design centers and research facilities around the world to serve the global electronics industry. More information about the company, its products and services is available at www.atrenta.com.

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