NEWS RELEASE

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ESD Alliance Adds Silvaco to Membership Roster
Silvaco's Warren Savage to Head Semiconductor IP Working Group

SAN JOSE, CALIF.— August 30, 2016 — The Electronic System Design (ESD) Alliance, an international association of companies providing goods and services throughout the semiconductor design ecosystem, today announced Silvaco, Inc. as its newest member.

Silvaco, a leading EDA provider of software tools used for process and device development and for analog/mixed-signal, power IC and memory design, joined because it has a growing portfolio of production-proven intellectual property (IP) cores.

Additionally, Warren Savage, general manager of its IP Division, was named chair of the ESD Alliance's Semiconductor IP (SIP) Working Group. The newly formed group will develop a common methodology and best practices for fingerprinting, and an end-to-end solution for tracking and auditing soft and hard IP.

Notes Savage: "The benefits of joining the ESD Alliance are numerous, especially the work coming from the various committees on such important subjects as export, license management and anti-piracy, interoperability and market statistics. We expect to be active and involved members and drivers of the SIP Working Group."

"We are pleased that Silvaco has joined the alliance," says Bob Smith, the ESD Alliance's executive director. "We worked closely with Warren to organize the SIP working group and appreciate having Silvaco's full support in helping us get this new initiative launched. With the rapid growth in the use of SIP, we are finding strong interest in the working group from across the ecosystem including IP users, IP developers and accounting/auditing firms."

About Silvaco, Inc.

Silvaco, Inc. is a leading EDA provider of software tools used for process and device development and for analog/mixed-signal, power IC and memory design. Silvaco delivers a full TCAD-to-signoff flow for vertical markets including: displays, power electronics, optical devices, radiation and soft error reliability and advanced CMOS process and IP development. Silvaco complements its design tools with a comprehensive portfolio of processor and networking IP for the automotive and IoT markets, and a cloud-based enterprise-level IP management solution. For over 30 years, Silvaco has enabled its customers to bring superior products to market at reduced cost and in the shortest time. The company is headquartered in Santa Clara, California and has a global presence with offices located in North America, Europe, Japan, China and Asia. For more information, go to: www.silvaco.com

About the Electronic System Design Alliance

The Electronic System Design (ESD) Alliance, an international association of companies providing goods and services throughout the semiconductor design ecosystem, is a forum to

address technical, marketing, economic and legislative issues affecting the entire industry. It acts as the central voice to communicate and promote the value of the semiconductor design industry as a vital component of the global electronics industry. For more information about the ESD Alliance, visit http://www.esd-alliance.org

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