

NEWS RELEASE



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Electronic System Design Alliance Elects Board of Directors to Two-Year Term *Solido President Amit Gupta Joins Board*

SAN JOSE, CALIF. — May 25, 2016 — The [Electronic System Design \(ESD\) Alliance](#) (formerly the EDA Consortium), an international association of companies providing goods and services throughout the semiconductor design ecosystem, today announced its newly elected nine-member Board of Directors who will serve a two-year term.

Returning board members are:

- Aart de Geus, chairman and chief executive officer (CEO) of Synopsys, Inc.
- Dean Drako, president and CEO at IC Manage
- John Kibarian, president, CEO and co-founder of PDF Solutions, Inc.
- Grant Pierce, Sonics' CEO
- Walden C. Rhines, chairman and CEO of Mentor Graphics Corporation
- Simon Segars, CEO at ARM
- Lip-Bu Tan, president and CEO of Cadence Design Systems

In addition, Lucio Lanza, managing director of Lanza *tech*Ventures, was appointed by the ESD Alliance board in April and Amit Gupta, president of Solido Design Automation, was elected to fill the remaining board seat.

The board was elected by the ESD Alliance's voting members during the voting period that ended May 20. The new board chair will be elected at the first meeting of the new board June 2.

“As the re-branded ESD Alliance expands its mission to embrace additional areas of the electronic system design ecosystem, it's gratifying to see the incumbent board re-elected, allowing us to continue on our new path,” remarks Bob Smith, its executive director. “I also welcome Amit, who has been an active member of the Alliance, to share his expertise and experience as a member of the board.”

“I have seen the value of the ESD Alliance for companies of all sizes, having founded multiple EDA companies and contributed to various Alliance activities,” adds Gupta. “I am honored to have been elected to join the distinguished board, and look forward to helping further expand the benefits of the Alliance.” □

The ESD Alliance at DAC

The ESD Alliance will exhibit at the Design Automation Conference (DAC) in Booth (#1920) Monday, June 6, through Wednesday, June 8, from 10 a.m. until 6 p.m. at the Austin Convention Center, Austin, Texas.

A longtime DAC sponsor, it will showcase its expanded mission and new initiatives to support the Design Ecosystem and host a DAC Pavilion (Booth #1839) talk, “Solving the Design Cost Puzzle: How intellectual property (IP) Fits.” Jim Feldhan, president of Semico Research, will address trends and issues driving the rapidly growing market for third-party IP Tuesday from 10:30 a.m. until 11 a.m.

The DAC program can be found at: www.dac.com

About the Electronic System Design Alliance

The [Electronic System Design \(ESD\) Alliance](http://www.esd-alliance.org), an international association of companies providing goods and services throughout the semiconductor design ecosystem, is a forum to address technical, marketing, economic and legislative issues affecting the entire industry. It acts as the central voice to communicate and promote the value of the semiconductor design industry as a vital component of the global electronics industry. For more information about the ESD Alliance, visit <http://www.esd-alliance.org>

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