

## ***NEWS RELEASE***



For more information, contact:

Nanette Collins  
Public Relations for the ESD Alliance  
(617) 437-1822  
[nanette@nvc.com](mailto:nanette@nvc.com)

### **AMIQ EDA of Bucharest, Romania, Joins the ESD Alliance**

*Notes Interoperability, Licensing and Export Committees as Vital Organization Elements*

**REDWOOD CITY, CALIF. — January 30, 2018** — The [Electronic System Design Alliance](#) (ESD Alliance), today confirmed [AMIQ EDA](#) of Bucharest, Romania, became a member of the growing international association of companies providing goods and services throughout the semiconductor design ecosystem.

AMIQ EDA, a pioneer in integrated development environments (IDEs) for hardware design and verification-specific languages such as SystemVerilog, Verilog, VHDL and *e*, joins other new ESD Alliance members [Breker Verification Systems](#) and [Minima Processor](#).

“We believe that alliances are essential to deliver best-in-class products, especially in the semiconductor industry,” affirms Cristian Amitroaie, chief executive officer (CEO) and founder of AMIQ EDA. “The ESD Alliance is an important venue for the electronic system design ecosystem to collaborate on issues our users care about, such as interoperability, conformance to

industry standards, and a reasonably consistent licensing model. As a software supplier, we also need to be aware of work with the various export regulations in the industry.”

AMIQ EDA’s DVT Eclipse IDE, a de facto standard for design and verification source code development, improves daily productivity by enabling faster and smarter code development and simplifying legacy code maintenance for novices and experts alike. Its Verissimo SystemVerilog testbench linter enables engineers to enforce group or corporate coding guidelines to ensure consistency and best practices in code development.

“We offer a hearty welcome to AMIQ EDA, a significant player in the industry and our first member from Romania,” says Bob Smith, executive director of the ESD Alliance. “Founded by former design consultants, it takes a user-centered design and verification approach and has a commitment to properly understanding and solving user requirements, both laudable achievements for any company.”

### **About AMIQ EDA**

[AMIQ EDA](#) adds value to design and verification domains through its code development and analysis tools. Its core solution — Design and Verification Tools (DVT) — is the first integrated development environment (IDE) for the *e* language, SystemVerilog and Verilog-AMS and VHDL. It helps engineers increase the speed and quality of code development and simplify maintenance and debugging, enabling them to complete their projects faster. For more information, visit [www.dvteclipse.com](http://www.dvteclipse.com)

### **About the Electronic System Design Alliance**

The [Electronic System Design \(ESD\) Alliance](#), an international association of companies providing goods and services throughout the semiconductor design ecosystem, is a forum to address technical, marketing, economic and legislative issues affecting the entire industry. It acts

as the central voice to communicate and promote the value of the semiconductor design industry  
as a vital component of the global electronics industry.

**Follow the ESD Alliance:**

Website: [esd-alliance.org](http://esd-alliance.org)

ESD Alliance Bridging the Frontier blog: <http://bit.ly/2oJUVzI>

Twitter: @ESDAlliance

LinkedIn: <https://www.linkedin.com/groups/8424092>

Facebook: <https://www.facebook.com/ESDAlliance>

*All trademarks and registered trademarks are the property of their respective owners.*