



Press Contact:  
Georgia Marszalek, ValleyPR, 650 345 7477, Georgia@ValleyPR.com

**BOARD OF DIRECTORS**

**BOARD OF DIRECTORS**

**CHAIRMAN:**

**Walden C. Rhines**  
Mentor Graphics Corp.

**VICE CHAIRMEN:**

**Jacques Benkoski**  
Monterey Design Systems

**Ray Bingham**  
Cadence Design Systems

**DIRECTORS:**

**Bernard Aronson**  
Synplicity, Inc.

**Aart de Geus**  
Synopsys, Inc.

**Moshe Gavrielov**  
Verisity Design

**Alan P. Naumann**  
CoWare

**Mike Tsai**  
Axis Systems

**Sang Wang**  
Nassda Corporation

## **RICHARD NEWTON RECEIVES EDA CONSORTIUM'S 10th ANNUAL KAUFMAN AWARD**

**SAN JOSE**, California, October 6, 2003—The EDA Consortium today announced that A. Richard Newton, Dean of the College of Engineering at the University of California, Berkeley, is this year's recipient of its prestigious Phil Kaufman Award.

Dr. Newton is being selected for his support and contributions to the EDA industry, including the pioneering work that is behind the success of many of today's commercial Electronic Design Automation (EDA) products. The Consortium will present the Award on Thursday, October 23, at its annual award ceremony at the Fairmont Hotel in San Jose.

"Professor Newton's contributions to advancing the EDA industry are numerous and significant, directly impacting the success of our users-- electronic designers," remarked Walden C. Rhines, EDA Consortium chairman and chairman and CEO of Mentor Graphics Corporation. "Richard has been a visionary and catalyst for advancing our industry in his role as mentor, educator, researcher and entrepreneur."

Richard Newton is also the Roy W. Carlson Professor of Engineering at the University of California, Berkeley, and a Professor in the Department of Electrical Engineering and Computer Sciences at Berkeley and was the founding Director of the MARCO/DARPA Gigascale Silicon Research Center for Design and Test from 1998-2002. His teaching and research interests include all aspects of the design of electronic systems and the application of information and communication technologies (ICT) to the solution of societal problems. For Richard Newton's full biography, please visit <http://www-cad.eecs.berkeley.edu/~newton/>

Aart de Geus, presenter of the Kaufman award and chairman and CEO of Synopsys, Inc., added, "Rich embodies the heart and soul of the EDA industry. His unfailing enthusiasm for the power of EDA to uphold Moore's Law serves as a continuing inspiration to us all. Speaking for myself, Synopsys owes a great debt of gratitude to Rich, for without his vision and dedication, we would not be the company that we are today."

### **More About Richard Newton's Career**

Dr. Newton began his EDA career as a student at the University of Melbourne, Australia, where he developed one of the first interactive versions of the SPICE circuit simulator in 1971, using an ASR33 teletype. He continued his work in circuit simulation at Berkeley, pioneering mixed-mode simulation and then developing with his students a timing analysis technique in the mid-1980s.

Professor Newton and his students have produced pioneering work in almost every area of electronic design automation, from multiprocessor-based circuit simulation to formal verification, from optimal circuit partitioning to probabilistic circuit placement techniques.

Dr. Newton introduced the notion of a "CAD Framework" to the research community in 1979—where a unified data model, open tool-based integration methodology and extension language are used to construct and evolve an EDA system. This work was used as the technical foundation for SDA Systems, now Cadence Design Systems. He was also part of the core team that developed EDIF (Electronic Design Interchange Format).

Dr. Newton has also played a key role in the formation of several successful EDA companies, including Synopsys, Inc., SDA, PIE Design Systems and Simplex Solutions (now part of Cadence) among others.

### **About the Phil Kaufman Award**

Initiated in 1994, the Phil Kaufman Award honors individuals who have made a substantial, sustainable contribution to the success and advancement of the EDA industry that benefits the industry's tools' users-- electronic designers.

### **About the EDA Consortium**

The EDA Consortium is the international association of companies that provide tools and services that enable engineers to create the world's electronic products. EDA is the critical technology used to design electronics for the communications, computer, space technology, medical and industrial equipment and consumer electronics markets among others.

Contact the EDA Consortium at, 111 West Saint John Street, Suite 220, San Jose, Calif. 95113, USA, office 408-287-3322, fax 408-283-5283 or visit [www.edac.org](http://www.edac.org).

###

The information supplied by the EDA Consortium is believed to be accurate and reliable, and the Consortium assumes no responsibility for any errors that may appear in this document. All trademarks and registered trademarks are the property of their respective owners.